

Docket No.: 071971-0432

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 53080
	:	
Satoshi SHIBATA	:	Confirmation Number: 2300
	:	
Patent No.: 7,737,012		
Application No.: 10/557,746	:	Group Art Unit: 2813
	:	Allowed: February 05, 2010
Filed: November 21, 2005	:	Examiner: Latanya N. CRAWFORD
	:	
For: MANUFACTURING METHOD OF A SEMICONDUCTOR DEVICE		

REQUEST FOR CERTIFICATE OF CORRECTION UNDER 37 CFR 1.322

Mail Stop Certificate of Correction
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reviewing the above-identified patent, a printing error was discovered therein requiring correction in order to conform the Official Record in the application.

The error noted is set forth on the attached copy of form PTO-1050 Rev. 2-93 in the manner required by the Commissioner's Notice.

Specifically, the section entitled --OTHER PUBLICATIONS-- and the literature reference --BORLAND "Low Temperature Activation of Ion Implanted Dopants: A Review", Japan Society of Applied Physics, 202, pp. 85-88-- has been erroneously omitted from the patent. Attached, please find our Information Disclosure Statement and PTO Form 1449 filed November 21, 2005, as well as the copy of the 1449 initialed by the Examiner which lists the omitted reference.

The change requested herein occurred as a result of printing the Letters Patent and the Certificate should be issued without expense under Rule 322 of the Rules of Practice. Accordingly, Applicants request issuance of the Certificate of Correction.

Please charge any shortage in fees due in connection with the filing of this paper to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

Michael E. Fogarty
Registration No. 36,139

**Please recognize our Customer No. 53080
as our correspondence address.**

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 MEF:sls
Facsimile: 202.756.8087
Date: September 8, 2010

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO : 7,737,012
APPLICATION NO. : 10/557,746
ISSUE DATE : June 15, 2010
INVENTOR(S) : Satoshi Shibata

Page 1 of 1

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

ON THE TITLE PAGE:

In Item "(56) References Cited", under "OTHER PUBLICATIONS" and
before the Examiner Information, insert the following:

– BORLAND "Low Temperature Activation of Ion
Implanted Dopants: A Review", Japan Society of
Applied Physics, 202, pp. 85-88 --

MAILING ADDRESS OF SENDER (Please do not use customer number below):

600 13th Street, N.W.
Washington, D. C. 20005-3096

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: ATTENTION Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Docket No.: 071971-0432

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
Satoshi SHIBATA	:	Confirmation Number: Not yet
assigned	:	
Application No.: Not yet assigned	:	Group Art Unit: Not yet assigned
Filed: November 21, 2005	:	Examiner: Not yet assigned
For: MANUFACTURING METHOD OF A SEMICONDUCTOR DEVICE	:	

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents and Trademarks
Washington, D. C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed with the application and no certification or fee is required.

A copy of the foreign search report is attached for the Examiner's information. Please note this is a PCT application in the entry of the National Phase in the U.S. and copies of the references cited were transmitted by WIPO and are believed to be in the file of the above identified application and readily available to the Examiner. Therefore it is believed that Applicants have met all requirements regarding duty of disclosure under 37

Not yet assigned

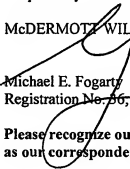
10/557746
IAP15 Rec'd PCT/PTO 21 NOV 2005

CFR 1.56. Acknowledgement and consideration of these documents are respectfully requested.

A copy of the foreign search report is attached for the Examiner's information. Please note this is a PCT application in the entry of the National Phase in the U.S. Since the Search Report was from the U.S. JPO or EPO search authorities, copies of these references should have been supplied to the USPTO under the trilateral agreement and are believed to be in the file of the above identified application and readily available to the Examiner.

Respectfully submitted,

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